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# Modeling and Control of the Modular Multilevel Converter connected to an inductive DC source using Energetic Macroscopic Representation

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## Keywords

« Modular Multilevel Converter (MMC) », «DC side inductor», «High Voltage Direct Current (HVDC) », «Power transmission».

## Abstract

The use of DC transmission is particularly advantageous for long-distance transmission and interconnection of asynchronous AC networks. Several converter topologies can be used for HVDC. Multilevel Modular Converters (MMCs) are the most favored given their technological advantages over other converters topologies. Due to their industrial maturity, they have become essential for all AC / DC conversion. So far, they have always been studied with a voltage source on DC side. However, when the converter is equipped with DC breaker, a series inductor is associated to limit current variations. This has consequences in term of modeling and control determination. This article aims to propose a modification of the control law in order to take into account this inductor. To facilitate the control organization, the Energetic Macroscopic Representation (EMR) is used.

## Introduction

The debate over AC or DC electric power transmission systems dates back to the late 19th century with the dispute between Thomas Edison's DC system and Westinghouse's AC technology. As a result, AC transmission systems became the predominant technology adopted. It was not until the 1930s, when the mercury arc valve was invented that the development of high voltage direct current (HVDC) transmission systems started [1]. In 1941, the first contract for a commercial HVDC system was placed and since then it became an alternative to AC transmission

HVDC technology is also the best solution to meet the demand for long-distance power transmission technologies thanks to their technical and economic advantages [2]. The first conversion technologies for HVDC applications were based on current source converters (CSCs) using thyristors. The introduction of power semiconductors with open/close capabilities has made possible the development of voltage source converters (VSCs). Recently, the modular multilevel converter (MMC) shown in Figure.1 has been quickly seen in a wide variety of applications as a result of significant advantages over previous technologies while retaining the properties of a conventional VSC.

With a sufficient number of levels, the output filter can be eliminated. In addition, since the switching frequency is lower for this converter, losses are reduced [3]. For the reasons stated, the MMC is a very interesting topology, not only for HVDC transmission, but also for other applications such as variable

speed drives, STATCOM, wind power generation. MMC technology has been widely accepted in the industry as it achieves high levels of power (1GW) and voltage (640kV).

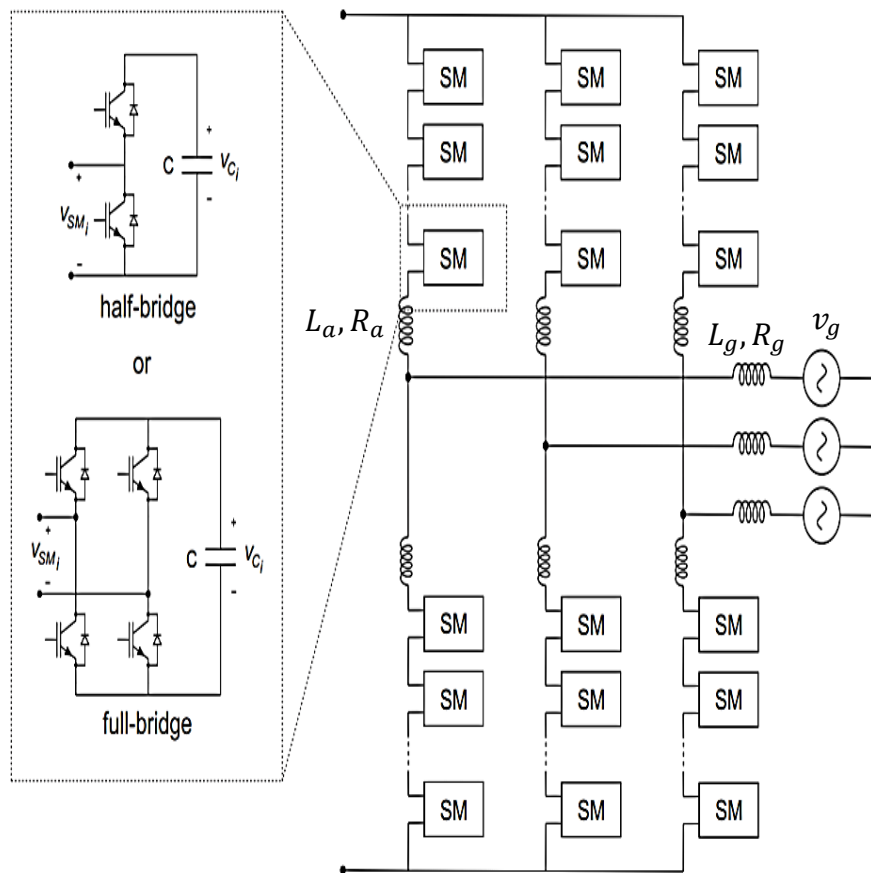


Fig.1: Modular Multilevel Converter structure

In a point-to-point HVDC link, a DC fault results in the interruption of the link by opening the AC circuit breakers. In case of MTDC network, it is necessary to isolate the faulty link using DC breaker circuit which remains a major challenge until now. Thanks to the rapid progress of the technology, it is possible now to cut a direct current in a specific point of the network with the devices "Direct Current Circuit Breaker (DCCB) [4]". These latter are usually put in series with inductors of large values. It is possible, in this case, to represent the DC side of the converter by a voltage source in series with an inductor as shown in Figure 2. The purpose of this paper is not particularly concerned with faults, but it is focused on the impact of this inductor on the operation of the system and its consideration on the control law. It is also possible to generalize this study to an inductor coming from a DC transmission line [5].

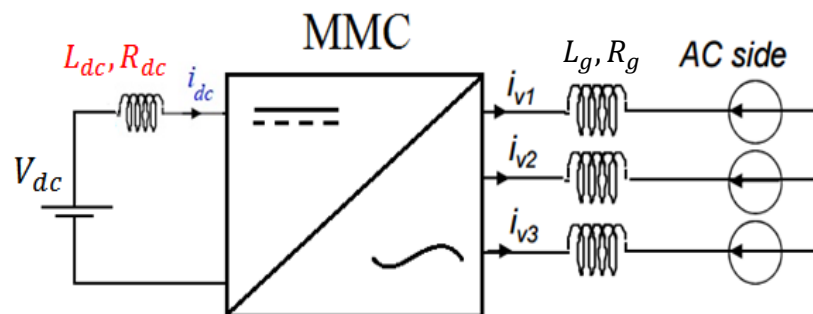


Fig.2: Simplified diagram of the MMC connected to an inductive DC source.

The conventional control strategies developed for an MMC connected to a DC voltage source [6-10] are no longer valid in the case of a strong inductor present on the DC bus, since this latter strongly influences the stability of the system as the shows the simulation below (Fig.3).

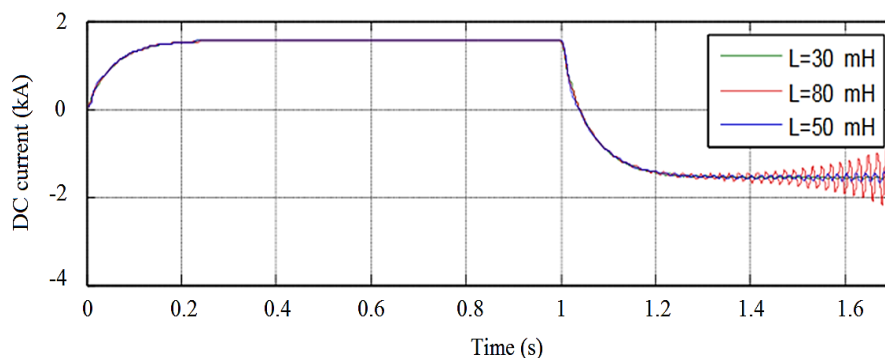


Fig.3: Conventional controls impact on the system stability

As it can be seen, the stability is no longer ensured in the case of a negative power flow. For this reason, it is important and necessary to have a suitable model of the MMC to integrate the impact of this inductor and subsequently to develop a suitable control law.

Most of the authors model the DC bus as a voltage source [6-10], the first work close to our problematic was done by [11] relying on several considerations and hypotheses at the modeling level. This latter considers that the DC power is equitably distributed in the three legs. This hypothesis is true in the case where the system is symmetrical with a perfect balanced network. But in case of a converter with many components (like the MMC), the symmetry of the system cannot be ensured (ex: losses in arms). Moreover, the balance of the network is not guaranteed [12].

The objective of this paper is to propose a new control law to keep the system stable for a high inductor value as the one considered in [4].

## System modeling

### Average model of the Modular Multilevel Converter

Given the large number of submodules (SMs), the study is based on the average model of the MMC considering that the capacitor voltages are well balanced [9]. Therefore, each arm is represented by an ideal DC / DC converter controlled by a modulation ratio defined as the ratio of the active submodule number divided by the total number of submodules, an equivalent capacitor of value  $C/N$ , and an equivalent voltage  $v_{c_{tot}} = v_1 + v_2 + v_3 \dots \dots + v_n$ .

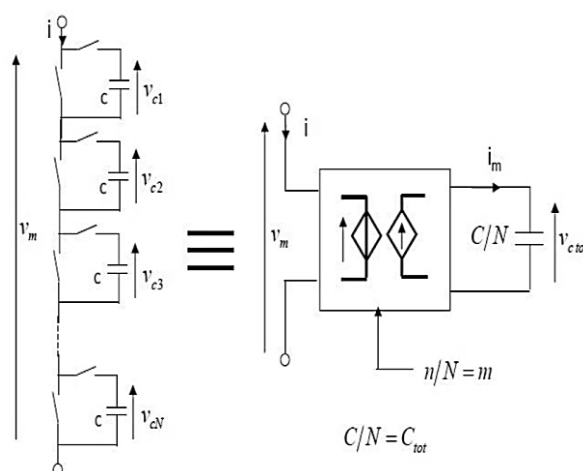


Fig. 4. Average model of the MMC-arm [9]

The modulated voltage and current of each arm  $v_m, i_m$  are respectively expressed as follow:

$$v_m = m \cdot v_{c_{tot}} \quad (1)$$

$$i_m = m \cdot v_m \quad (2)$$

We can therefore deduce the evolution of the modulated current by the following equation:

$$i_m = C_{tot} \cdot \frac{d}{dt}(v_{c_{tot}}) \quad (3)$$

### System equation

According to Kirchhoff's law, two equations are obtained from the inner loop of the MMC allowing a first modeling ( $x = a, b, c$ ):

$$\frac{\bar{V}_{dc}}{2} - \frac{L_{dc}}{2} \cdot \frac{d\bar{i}_{dc}}{dt} - \frac{R_{dc}}{2} \cdot \bar{i}_{dc} - \bar{v}_{mu_x} - L_a \cdot \frac{d\bar{i}_{mu_x}}{dt} - R_a \cdot \bar{i}_{mu_x} = \bar{v}_{g_x} + L \cdot \frac{d\bar{i}_{g_x}}{dt} + R \cdot \bar{i}_{g_x} - \bar{v}_{n0} \quad (4)$$

$$-\frac{\bar{V}_{dc}}{2} + \frac{L_{dc}}{2} \cdot \frac{d\bar{i}_{dc}}{dt} + \frac{R_{dc}}{2} \cdot \bar{i}_{dc} + \bar{v}_{ml_x} + L_a \cdot \frac{d\bar{i}_{ml_x}}{dt} + R_a \cdot \bar{i}_{ml_x} = \bar{v}_{g_x} + L \cdot \frac{d\bar{i}_{g_x}}{dt} + R \cdot \bar{i}_{g_x} - \bar{v}_{n0} \quad (5)$$

The modeling of the AC side is done by the summation of the equations (4) and (5):

$$v_{m_{l_x}} - v_{m_{u_x}} + L_a \frac{d}{dt}(i_{m_{l_x}} - i_{m_{u_x}}) + R_a (i_{m_{l_x}} - i_{m_{u_x}}) = 2(v_{g_x} + L_g \frac{d}{dt}(i_{g_x}) + R_g \frac{d}{dt}(i_{g_x})) \quad (6)$$

Considering the variable change as in [13], with  $i_{u_x} = i_{g_x} + i_{l_x}$ :

$$v_{v_x} = \frac{v_{m_{l_x}} - v_{m_{u_x}}}{2} \quad (7)$$

$$v_{diff_x} = \frac{v_{m_{l_x}} + v_{m_{u_x}}}{2} \quad (8)$$

$$i_{diff_x} = \frac{i_{l_x} + i_{m_x}}{2} \quad (9)$$

The fusion of equations (6) and (7) defines the equations vector of the state variables  $i_{g_x}$ :

$$v_{v_x} - v_{g_x} = \left(\frac{L_a}{2} + L_g\right) \frac{d}{dt}(i_{g_x}) + \left(\frac{R_a}{2} + R_g\right) (i_{g_x}) \quad (10)$$

Conventionally, the line currents are controlled in Park domain.

In a similar way to the modeling of the AC side, the integration of (8) and (9) in the difference of the equations (4) and (5) allows the modeling of the DC bus:

$$\frac{V_{dc}}{2} - v_{diff_x} = \frac{L_{dc}}{2} \cdot \frac{d}{dt}(i_{dc}) + \frac{R_{dc}}{2} (i_{dc}) + L_a \cdot \frac{d}{dt}(i_{diff_x}) + R_a (i_{diff_x}) \quad (11)$$

It is clear that the equation (11) contains coupled state variables since  $i_{dc} = i_{diff_a} + i_{diff_b} + i_{diff_c}$ . This coupling is due to the presence of the inductor on the DC side. As a result, the differential  $i_{diff_x}$  can no longer be controlled independently as in the case of an MMC connected to a DC voltage source. One solution consists in defining another model, compared to the one usually used for the MMC, by defining a new variable change allowing the decoupling of the system variables.

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The diagram illustrates the control architecture for a VSC-HVDC system. It features two main power processing stages: a DC link and an AC output stage.

- DC Link Stage:** The DC voltage  $V_{dc}$  is fed into a series of three red blocks representing power electronics. The current  $i_{dc}$  flows from left to right. A feedback loop takes the DC voltage  $V_{dc}$ , compares it with a reference  $\vec{V}_{dc}^*$ , and generates a differential current  $i_{diffn}$ . This current is integrated to produce  $i_{diff}$ , which is then used to generate a differential voltage  $v_{diffn}$ .
- AC Output Stage:** The AC voltage  $V_g$  is fed into another set of three red blocks. The grid current  $i_g$  flows from right to left. A feedback loop takes the AC voltage  $V_g$ , compares it with a reference  $\vec{V}_g^*$ , and generates a differential current  $i_{gdqn}$ . This current is integrated to produce  $i_{gdq}$ , which is then used to generate a differential voltage  $v_{vdq}$ .
- Control and Reference Generation:** Two PI controllers are shown as blue trapezoidal blocks. They take the differential currents  $i_{diffn}^*$  and  $i_{gdqn}^*$  as inputs and generate reference voltages  $v_{diffn}^*$  and  $v_{vdq}^*$ . These references are combined to form the final AC voltage reference  $V_{mu/l}^*$ .
- Power Flow and Modulation:** The final AC voltage reference  $V_{mu/l}^*$  is modulated by a modulation index  $m_{w/l}$  (indicated by a red arrow) to produce the final AC output voltage  $V_{mu/l}$ . The total current  $i_{ctot}$  is also shown flowing through the system.

To avoid possible instability issues due to non-control of capacitor voltages  $v_{ctot}$ , an energy-based control strategy is developed in order to obtain a maximum control structure.

$$i_{diff_x} = i_{diff_{x_{AC}}} + i_{diff_{x_{DC}}} \quad (22)$$

$$i_{diff_{x_n}} = i_{diff_{x_n AC}} + i_{diff_{x_n DC}} \quad (23)$$

In steady state  $v_{diffx} \approx \frac{v_{dc}}{2}$ , and  $v_v \approx v_g$ . These considerations lead to two equations allowing the control of the fictitious AC differential currents through the control of the energy difference  $W_x^\Delta$  between upper and the lower arms, and the control of the fictitious DC differential currents through the control of the energy sum  $W_x^\Sigma$ :

$$\frac{d}{dt}W_x^\Sigma = \langle V_{dc} \cdot i_{diff_{x_{nDC}}} \cdot P - v_{gx} \cdot i_{gx} \rangle \quad (24)$$

$$\frac{d}{dt}W_x^\Delta = \langle -2v_{g_x} \cdot i_{g_x} - 2v_{g_x} \cdot i_{diff_{x_{n_{AC}}}} \cdot P \rangle \quad (25)$$

The control law of the energy based model (eq.24 and 25) is obtained by a direct model inversion:

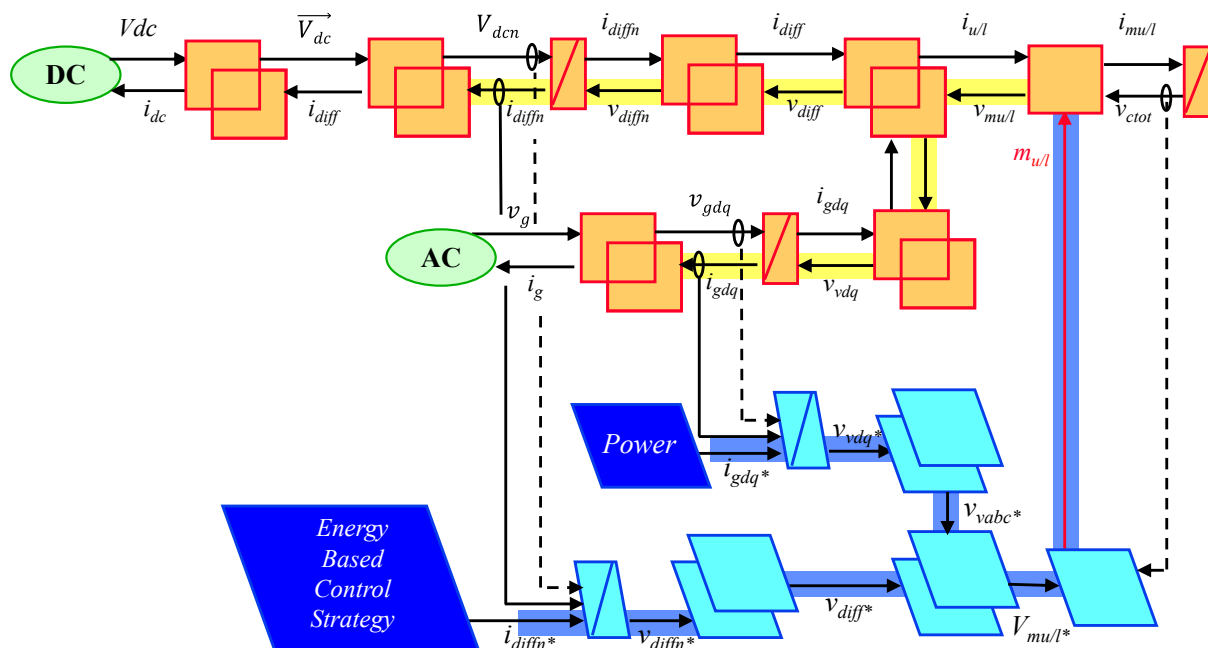


Fig. 7. Maximal control structure of the Modular Multilevel Converter

To validate the proposed control law, the simulations were performed on Matlab/Simulink. The parameters used are indicated on Tab.1. These latter come from the HVDC INELF link [16], and the inductor value used on the DC side comes from the reference [4].

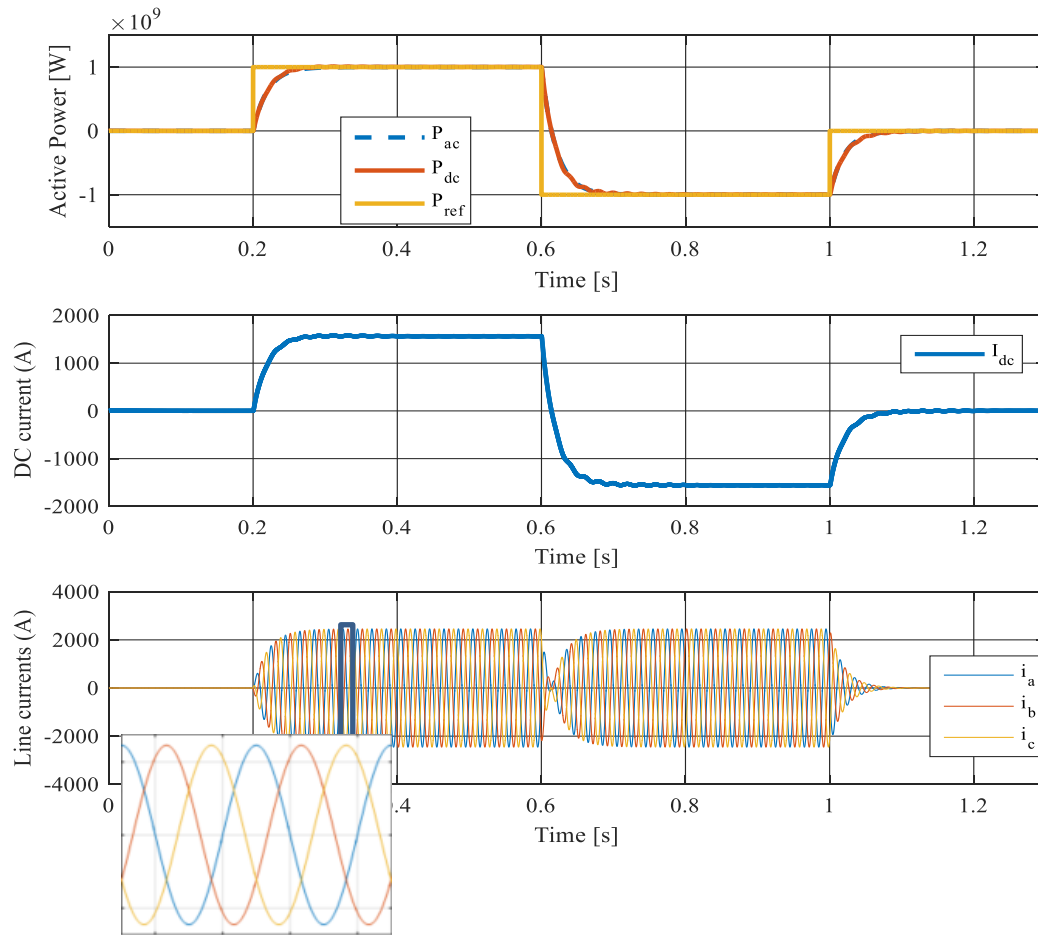


Only the variables at the input and at the output of the converter are presented in Fig. 8. At first, the power set point is initialized to zero until  $t = 0.2\text{s}$ , then  $P = 1\text{GW}$  is imposed until  $t = 0.6\text{s}$ , then  $-1\text{GW}$  until  $t = 1\text{s}$ .

**Table I. System parameters**

$P_n$	1 GW	$R_a$	50 mH
$V_{res}$	192 kV	$L_a$	50 mH
$f_{res}$	50 Hz	$R_{dc}$	300 mΩ
$V_{dc}$	640 kV	$L_{dc}$	300 mH
$R$	60 mH	$C_{tot}$	25 μF
$L$	60 mH		

It is remarkable that DC and AC powers follow the reference with a negligible difference due to Joule losses (the switches are supposed perfect). The line currents are sinusoidal and well balanced, and the DC side current side is continuous in steady state for each power set point. The system remains stable even with high  $L_{dc}$  value which was not the case for the control of the MMC connected to a voltage source (Fig.3).



**Fig. 8. Input and output variables of the converter**

Figure 9 shows internal variables of the converter. It can be seen that the arms currents consist of a DC component and an AC component, the sum of the differential currents is equal to the DC current and the average values of the differential currents are almost equal, which means that the power is evenly distributed on the converter arms. The voltages across the capacitors are centered on their 640kV reference value.

The initial results of the simulation show that the system is stable since all state variables are well controlled.

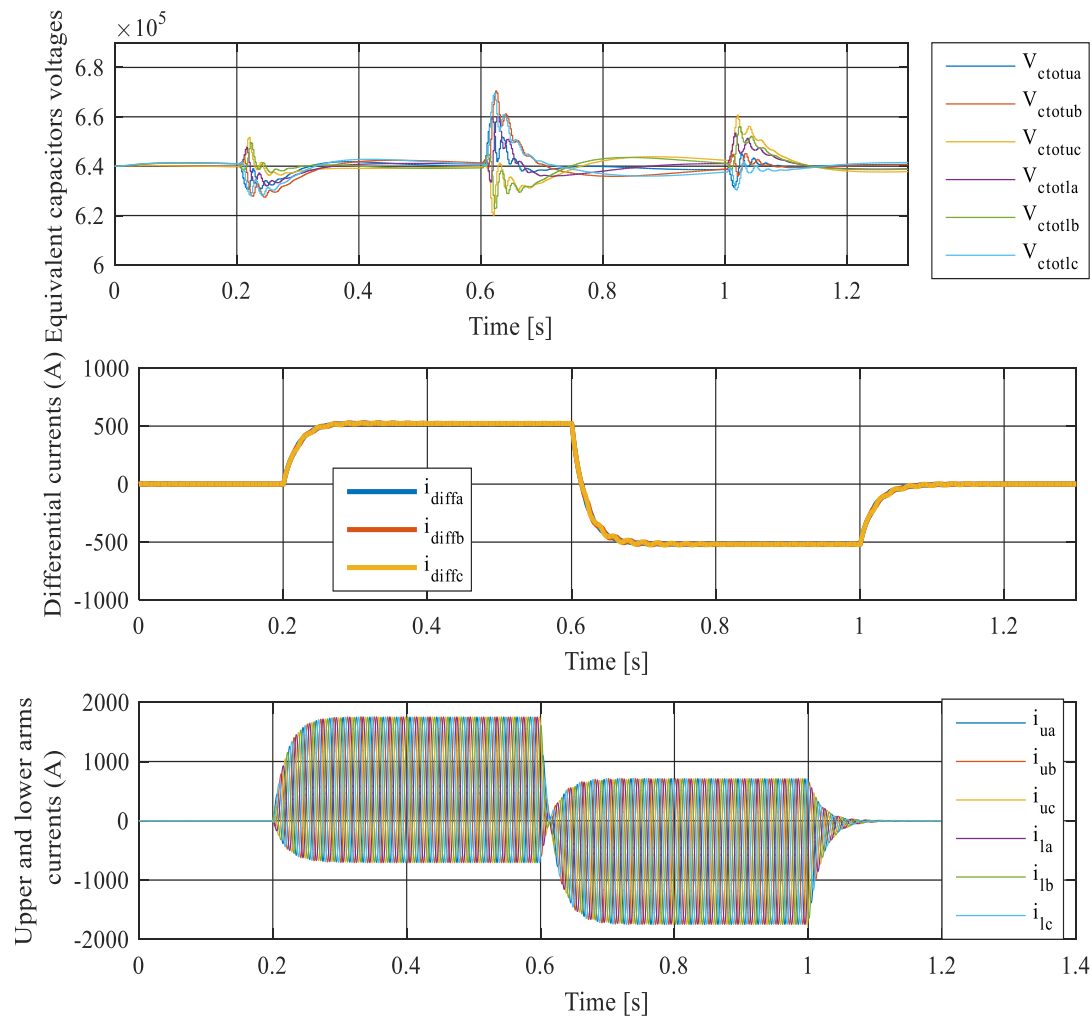

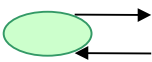


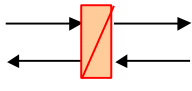
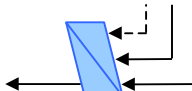
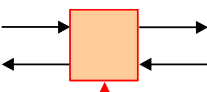
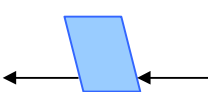
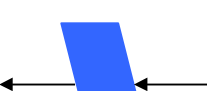
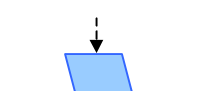
Fig. 9. Internal variables of the converter

## Conclusion

The first part of this paper allowed detailing the structure of the Modular Multilevel Converter, poses the issue related to the presence of an inductor in its DC bus, explains its operating principle, and its mathematical model. This part showed at first, the coupling of the system states due to the presence of the inductive node, thus, a methodology to solve this problem was proposed. The second part dealt with the converter control (line currents, the differential currents and the energy stored in the equivalent capacitors). Unlike other control strategies, all state variables have been controlled to avoid system instability. This ensures a good dynamic behavior and increases the system robustness.

## Appendix

ENERGETIC MACROSCOPIC REPRESENTATION (EMR) [14]			
EMR is a systemic extension of COG, based on the interaction principle.			
	Action and reaction variables		Energy source (system terminals)

	Energy accumulation (energy storage)		indirect inversion (closed-loop control)
	Mono-physical converter (energy conversion)		direct inversion (open-loop control)
	Strategy (energy management)		direct inversion using a disturbance rejection

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